

SHOW FILE;DS

File 748:Asia/Pac Bus. Jrnls 1994-2005/Oct 31
(c) 2005 Dialog
File 759:Business Insights 1992-2005/Oct
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File 810:Business Wire 1986-1999/Feb 28
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File 813:PR Newswire 1987-1999/Apr 30
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File 122:Harvard Business Review 1971-2005/Sep
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File 9:Business & Industry(R) Jul/1994-2005/Nov 03
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File 275:Gale Group Computer DB(TM) 1983-2005/Nov 03
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File 608:KR/T Bus.News. 1992-2005/Nov 04
(c)2005 Knight Ridder/Tribune Bus News
File 674:Computer News Fulltext 1989-2005/Oct W2
(c) 2005 IDG Communications
File 35:Dissertation Abs Online 1861-2005/Oct
(c) 2005 ProQuest Info&Learning
File 99:Wilson Appl. Sci & Tech Abs 1983-2005/Oct
(c) 2005 The HW Wilson Co.
File 610:Business Wire 1999-2005/Nov 04
(c) 2005 Business Wire.

Set	Items	Description
S1	1	(DECRYPT? (S) ENCRYPT? (S) (BLACK? (W) BOX)) AND PD<=990-327
S2	3	(CRYPTO? (S) (BLACK? (W) BOX)) AND PD<=990327
S3	0	(DECRYPT? (S) ENCRYPT? (S) ("DRM" OR (DIGITAL (W) RIGHTS (-W) MANAGEMENT))) AND PD<=990327
S4	0	(DECRYPT? AND ENCRYPT? AND ("DRM" OR (DIGITAL (W) RIGHTS (-W) MANAGEMENT))) AND PD<=990327
?		

B 748,759,810,813,122,9,275,608,674,35,99,610,476,613,634,64
04nov05 05:37:37 User264717 Session D349.1
\$0.00 0.203 DialUnits FileHomeBase
\$0.00 Estimated cost FileHomeBase
\$0.13 INTERNET
\$0.13 Estimated cost this search
\$0.13 Estimated total session cost 0.203 DialUnits

SYSTEM:OS - DIALOG OneSearch

File 748:Asia/Pac Bus. Jrnl 1994-2005/Oct 31

(c) 2005 Dialog

File 759:Business Insights 1992-2005/Oct

(c) 2005 Datamonitor

File 810:Business Wire 1986-1999/Feb 28

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File 275:Gale Group Computer DB(TM) 1983-2005/Nov 03

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(c) 2005 ProQuest Info&Learning

File 99:Wilson Appl. Sci & Tech Abs 1983-2005/Oct

(c) 2005 The HW Wilson Co.

File 610:Business Wire 1999-2005/Nov 04

(c) 2005 Business Wire.

***File 610: File 610 now contains data from 3/99 forward.**

Archive data (1986-2/99) is available in File 810.

File 476:Financial Times Fulltext 1982-2005/Nov 03

(c) 2005 Financial Times Ltd

File 613:PR Newswire 1999-2005/Nov 04

(c) 2005 PR Newswire Association Inc

***File 613: File 613 now contains data from 5/99 forward.**

Archive data (1987-4/99) is available in File 813.

File 634:San Jose Mercury Jun 1985-2005/Nov 02

(c) 2005 San Jose Mercury News

File 640:San Francisco Chronicle 1988-2005/Nov 03
(c) 2005 Chronicle Publ. Co.

Set	Items	Description
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is a hardwired device costing 275 pounds sterling. The secur
protects databases on...

19850306

2/3,KWIC/3 (Item 2 from file: 275)

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01014280 SUPPLIER NUMBER: 00508157

Solution For Piracy.

Which Computer, p29

Nov., 1983

DOCUMENT TYPE: product announcement

LANGUAGE: ENGLISH

RECORD TYPE: ABSTRACT

ABSTRACT: Padlock is a device degigned to prevent software
device consists of a black box that uses the Data Encryp
(DES) and a pubic key cryptography that unlocks the DES-en
Padlock autodestructs when there is tampering with the syste

19831100

?

T S2/3,KWIC/1-3

2/3,KWIC/1 (Item 1 from file: 9)

DIALOG(R)File 9:Business & Industry(R)
(c) 2005 The Gale Group. All rts. reserv.

01451058 Supplier Number: 24139923 (USE FORMAT 7 OR 9 FO
Finland - Data Fellows Secures ICSA Certification
(Data Fellows has announced that its cryptography products
certified by the International Computer Security Associatio
Newsbytes News Network, p N/A
January 07, 1998
DOCUMENT TYPE: Journal (United States)
LANGUAGE: English RECORD TYPE: Fulltext
WORD COUNT: 348

(USE FORMAT 7 OR 9 FOR FULLTEXT)

TEXT:

...they will use," said Dr. Peter Tippett, the ICSA's presid

"To become ICSA Certified, cryptography products must init
rigorous suite of analysis and testing of cryptographic qu
includes assurances of the integrity and quality of all cry
algorithms used in the product, lack of major implementation
black - box testing' along with resistance to automated att
Data Fellows' Web site is at...

2/3,KWIC/2 (Item 1 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2005 The Gale Group. All rts. reserv.

01116377 SUPPLIER NUMBER: 00612986

Black Box Crypton Defies the Hackers.

Electronics Weekly, n1257, p26

March 6, 1985

DOCUMENT TYPE: product announcement ISSN: 0013-5224
ENGLISH RECORD TYPE: ABSTRACT

Black Box Crypton Defies the Hackers.

ABSTRACT: Black Box has a new data-link scrambler calle

<http://www.dialogclassic.com/main.vmgw>

11/4/2005

T S2/FULL/1

2/9/1 (Item 1 from file: 9)

DIALOG(R)File 9:Business & Industry(R)

(c) 2005 The Gale Group. All rts. reserv.

01451058 Supplier Number: 24139923 (THIS IS THE FULLTEXT)

Finland - Data Fellows Secures ICSA Certification

(Data Fellows has announced that its cryptography products have been certified by the International Computer Security Association (ICSA))

Newsbytes News Network, p N/A

January 07, 1998

DOCUMENT TYPE: Journal (United States)

LANGUAGE: English RECORD TYPE: Fulltext

WORD COUNT: 348

TEXT:

HELSINKI, FINLAND, 1998 JAN 7 (NB) -- By Steve Gold, Newsbytes. Data Fellows has announced that its cryptography products have been certified by the International Computer Security Association (ICSA).

According to officials with the Finnish company, perhaps best known for its F-Prot anti-virus package, the firm's F-Secure VPN and F-Secure SSH Client plus Server packages are among the first encryption products in the industry to achieve this certification.

Petri Laakkonen, the company's president, said that receiving "two thumbs up" from the global expert in network security in their debut certification of cryptographic systems is a major step for the firm.

Newsbytes notes that the packages receiving ICSA certification are software that offer users military grade encryption technology. F-Secure VPN is claimed to connect separate local area networks (LANs) together over the Internet, creating an encrypted virtual private network. The package is claimed to work with existing firewalls and routers and does not require any special hardware or proprietary systems.

F-Secure SSH, meanwhile, is claimed to allow Windows, Unix, and Macintosh users to have secure e-mail, X-Windows, Web browser, Telnet, database, and other TCP/IP (Transmission Control Protocol/Internet Protocol) connections over untrustworthy networks, such as the Internet.

F-Secure SSH is claimed to provide both host and user authentication, automatic military grade session encryption, and integrity protection for all transferred data. All F-Secure products are billed as using RSA secured, industry standard SSH transport layer protocol and its military strength encryption methods, such as triple-DES, Blowfish, and Idea. "Even sophisticated users have no real way to judge the quality of cryptography implemented in the products and system they will use," said Dr. Peter Tippet, the ICSA's president.

"To become ICSA Certified, cryptography products must initially pass a rigorous suite of analysis and testing of cryptographic quality which includes assurances of the integrity and quality of all cryptographic algorithms used in the product, lack of major implementation errors, and 'black-box testing' along with resistance to automated attacks," he said.

Data Fellows' Web site is at <http://www.europe.datafellows.com> .

Reported by Newsbytes News Network, <http://www.newsbytes.com> .

(19980107/Press & Reader Contact: Data Fellows Europe +358-9-478444; Fax +358-9-4784-4599; Data Fellows US 408-938-6700; Fax +408-938-6701; E-mail: info@datafellows.com)

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COMPANY NAMES: DATA FELLOWS

INDUSTRY NAMES: Network hardware and software; Software

PRODUCT NAMES: Networking software packages (737255)

CONCEPT TERMS: All product and service information; Product introduction

GEOGRAPHIC NAMES: European Union (EUCX); Finland (FIN); Scandinavia (SCAX); Western Europe (WEEX)

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T S2/FULL/2

2/9/2 (Item 1 from file: 275)

DIALOG(R) File 275:Gale Group Computer DB(TM)

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01116377 SUPPLIER NUMBER: 00612986

Black Box Crypton Defies the Hackers.

Electronics Weekly, n1257, p26

March 6, 1985

DOCUMENT TYPE: product announcement

ISSN: 0013-5224

LANGUAGE:

ENGLISH

RECORD TYPE: ABSTRACT

ABSTRACT: Black Box has a new data-link scrambler called Crypton. It is a hardwired device costing 275 pounds sterling. The security device protects databases on all types of microcomputers and allows only users with identically wired scramblers to receive intelligent data when dialling into a computer. A photograph of the product is included.

DESCRIPTORS: New Product; Microcomputer; Minicomputer; Database; Access Controls; Cryptography; Security Systems; Data Security

TRADE NAMES: Crypton

FILE SEGMENT: CD File 275

?

T S2/FULL/3

2/9/3 (Item 2 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)
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01014280 SUPPLIER NUMBER: 00508157

Solution For Piracy.

Which Computer, p29

Nov., 1983

DOCUMENT TYPE: product announcement

LANGUAGE: ENGLISH

RECORD TYPE: ABSTRACT

ABSTRACT: Padlock is a device degigned to prevent software piracy. This device consists of a black box that uses the Data Encryption Standard (DES) and a pubic key cryptography that unlocks the DES-encoded software. Padlock autodestructs when there is tampering with the system.

DESCRIPTORS: Software Piracy; Security Systems; Functional Capabilities

TRADE NAMES: Padlock

FILE SEGMENT: CD File 275

?

T S1/FULL/1

1/9/1 (Item 1 from file: 9)

DIALOG(R)File 9:Business & Industry(R)

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01308630 Supplier Number: 23957466 (THIS IS THE FULLTEXT
**EDA Software -- Cores target at 6000 FPGAs -- Atmel release
macrofunctions**

**(Atmel Corp will release DSP Designer 4.0 suite to help use
its AT6000 field programmable gate arrays; suite includes s
software that allows automatic inferencing and instantiatio**

Electronic Engineering Times, p 47

July 07, 1997

DOCUMENT TYPE: Journal ISSN: 0192-1541 (United States)

LANGUAGE: English RECORD TYPE: Fulltext

WORD COUNT: 552

ABSTRACT:

Atmel Corp will release a new tool set and 20 new DSP macrof help users design with its AT6000 field programmable gate ar DSP Designer 4.0 suite includes synthesis software from a st that Atmel has helped fund, and allows automatic inferencing instantiation of macros. The SRAM-based AT6000 is a dynamica reconfigurable FPGA aimed primarily at DSP and compute-inten applications. It claims clock speeds over 350 MHz and allows designs. The 20 new macrofunctions are fully defined, placed making them "hard" cores. They include regular and symmetric 24- and 32-tap FIR filters; a two-dimensional convolver; and 4 x 4, 5 x 5, and 6 x 6 data packet-switchers; data and para encryptors and decryptors; high-speed serial pattern detecto averager; a 24 x 24 crosspoint switch; and several UARTs.The can be "softened" because users can change the layouts to cu blocks. In addition to these functions, Atmel has some 50 co generators that produce parametized elements such as multipl accumulators, FIFOs and RAMs. Users can change the layout of functions as well. The component generators aren't new, but Synthesis Gateway product is. This offering contains Everest (San Francisco, CA) Everest EDS, a Verilog synthesis tool optimiz Atmel architecture that will support VHDL in the near future provides a complete synthesis capability for the AT6000, an isn't strictly needed, except for simulation. But Synthesis also accept synthesized net-lists from Cadence, Mentor Graph

Synopsys, Synario and VeriBest tools. Operator inferencing a Gateway to infer and generate a macrofunction by recognizing logic operators in the net-list. Component instantiation all synthesis tool to treat a macro as a black box, rather than gate-level net-list of it. Synthesis Gateway then recognizes places it in the layout and creates VHDL or Verilog code for DSP Designer 4.0 also has automatic partitioning for mapping multiple chips, and placing and routing them in adherence to timing constraints. A graphical browser shows the area utilized and outputs of each chip, and the interchip interconnect on diagram. The full-text article provides a little more detail

TEXT:

Byline: Richard Goering

San Jose, Calif. - Atmel Corp. this week releases a new tool DSP macrofunctions to help users design with its AT6000 field gate arrays. Atmel's DSP Designer 4.0 suite includes synthesis from a startup company that Atmel has helped fund, and allow inferencing and instantiation of macros.

The SRAM-based AT6000 is a dynamically reconfigurable FPGA at DSP and compute-intensive applications. It claims clock speeds in MHz and allows pipelined designs.

The 20 new macrofunctions are fully defined, placed and routed "hard" cores. They include regular and symmetrical 8-, 16-, FIR filters; a two-dimensional convolver; and edge detector; and 6 x 6 data packet-switchers; data and parallel-mode data decryptors; high-speed serial pattern detectors; a pixel array 24 crosspoint switch; and several UARTs.

Joel Rosenberg, director of marketing for FPGAs, noted that functions can be "softened" because users can change the layout and customize the blocks. In addition to these functions, Atmel component generators that produce parameterized elements such as adders, accumulators, FIFOs and RAMs. Users can change the logic functions as well.

Synthesis Gateway

The component generators aren't new, but Atmel's Synthesis Gateway

is. This offering works with third-party synthesis tools to operator inferencing and component instantiation of both the macrofunctions and the component generators. Further, it con EDS, a Verilog synthesis tool optimized for the Atmel archit

Rosenberg said that Everest is a San Francisco-based startup intends to compete in the commercial synthesis arena, and he product to the Synplify offering from Synplicity. The compan low-cost synthesis that can accept timing and area constrain EDS will support VHDL in the near future, Rosenberg said.

Since Everest provides a complete synthesis capability for t outside tool isn't strictly needed, except for simulation. B Gateway can also accept synthesized net-lists from Cadence, Graphics, Exemplar, Synopsys, Synario and VeriBest tools.

Operator inferencing allows Synthesis Gateway to infer and g macrofunction by recognizing arithmetic or logic operators net-list. Component instantiation allows the synthesis tool macro as a black box, rather than generating a gate-level ne Synthesis Gateway then recognizes the macro, places it in t creates VHDL or Verilog code for simulation.

Another new feature in DSP Designer 4.0 is automatic partiti can map logic between multiple chips, and place and route th to the user's timing constraints. A graphical browser shows utilization, inputs and outputs of each chip, and the interc interconnect on a block diagram.

The tool set also provides Atmel's first timing-driven place routing capability. DSP Designer can take timing constraints or a schematic, and a static timing analyzer will check the adherence to these constraints. At present, this is a post-p capability.

Also included in DSP Designer 4.0 is QuickChange, a paramet tool announced last year that supports the development of dy reconfigurable designs. Other features include interactive p routing. The tool includes interfaces to simulation tools f Synario, Viewlogic, VeriBest, Cadence and Mentor. DSP Design at \$995, is available now for Windows platforms.

Call (800) 292-8635

www.atmel.com.

Reader Service No. 614

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COMPANY NAMES: ATMEL CORP

INDUSTRY NAMES: Applications software; Software

PRODUCT NAMES: Manufacturing, engineering or design softwar
(737272)

CONCEPT TERMS: All product and service information; Product

GEOGRAPHIC NAMES: North America (NOAX); United States (US
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